

AMENDMENTS TO THE SPECIFICATION

Please replace paragraph [0001] with the following amended paragraph:

[0001] This application is related to the following applications, each of which is filed on the same date as this application and is hereby incorporated by reference in its entirety: System and Method For Simulating A Circuit Having Hierarchical Structure, application number 10/724,277 attorney docket number 010585-0013-999; System And Method For Adaptive Partitioning Of Circuit Components During Simulation, application number 10/713,751, U.S. patent number 7,024,652 attorney docket number 010585-0017-999; System And Method For Communicating Simulation Solutions Between Circuit Components In A Hierarchical Data Structure, application number 10/713,754 attorney docket number 010585-0018-999; System And Method For Supporting Multi-rate Simulation Of A Circuit Having Hierarchical Data Structure, application number 10/713,753 attorney docket number 010585-0019-999 and System And Method For Dynamically Representing Repetitive Loads Of A Circuit During Simulation, attorney docket number 010585-0020-999 application number 10/713,728.